



Dunlop 13-6

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): Dunlop et al.  
Case: 13-6  
Serial No.: 09/716,977  
Filing Date: November 20, 2000  
Group: 2634  
Examiner: Eva Y. Zheng

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

Signature:

Date: June 15, 2005

Title: Gated Clock Recovery Circuit

AMENDMENT TRANSMITTAL

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Submitted herewith are the following documents relating to the above-identified patent application:

1. Amendment After Allowance Under 37 CFR §1.312;
2. Marked-Up Specification; and
3. Substitute Specification.

There is no additional claim fee due in connection with the Amendment. In the event of non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit **Deposit Account No. 50-0762** as required to correct the error. A duplicate copy of this letter is enclosed.

Respectfully submitted,

*Kevin M. Mason*

Kevin M. Mason  
Attorney for Applicants  
Reg. No. 36,597  
Ryan, Mason & Lewis, LLP  
1300 Post Road, Suite 205  
Fairfield, CT 06824  
(203) 255-6560

Date: June 15, 2005

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15            AMENDMENT AFTER ALLOWANCE UNDER 37 C.F.R. §1.312

Mail Stop Issue Fee  
Commissioner for Patents  
P.O. Box 1450  
20     Alexandria, VA 22313-1450

Sir:

25            A Notice of Allowance and Issue Fee Due, mailed May 5, 2005 have been received  
in connection with the above-identified patent application. Please amend the above-identified  
allowed patent application as indicated on the attached sheets.